

APPLICATION DATA SHEET

APPLICATION INFORMATION

Filing Date:: 08-20-2003
Application Type:: Regular
Subject Matter:: Utility
Title:: Post-Layout Optimization in
Integrated Circuit Design
Attorney Docket Number:: X-946 US
Request for Early Pub?:: No
Request for Non-Pub?:: Yes
Total Drawing Sheets:: 06
Small Entity?:: No
Petition included?:: No

APPLICANT INFORMATION

Applicant Authority Type:: Inventor
Primary Citizenship Ctry:: Canada
Status:: Full Capacity
Given Name:: Jason
Middle Name:: H.
Family Name:: Anderson
Street:: 11 Joseph Salsberg Lane
City:: Toronto
State or Province:: Ontario
Country:: Canada
Postal or Zip Code:: M6I 3W8

Applicant Authority Type:: Inventor
Primary Citizenship Ctry:: US
Status:: Full Capacity
Given Name:: Sandor
Middle Name:: S.
Family Name:: Kalman
Street:: 734 Hilmar Street

City:: Santa Clara
 State or Province:: California
 Postal or Zip Code:: 95050

 Applicant Authority Type:: Inventor
 Primary Citizenship Ctry:: India
 Status:: Full Capacity
 Given Name:: Vinay
 Family Name:: Verma
 Street:: 3638 Springbrook Avenue
 City:: San Jose
 State or Province:: California
 Postal or Zip Code:: 95148

CORRESPONDENCE INFORMATION

Correspondence Customer Number:: 24309

REPRESENTATIVE INFORMATION

Representative Customer Number::	24309	
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DOMESTIC PRIORITY INFORMATION

Application::	Continuity Type::	Parent Application::	Parent Filing Date::
This Applic.	Non-Provisional	60/406,137	08-26-02

ASSIGNEE INFORMATION

Assignee Name:: Xilinx, Inc.
 Street:: 2100 Logic Drive
 City:: San Jose
 State or Province:: California
 Postal or Zip Code:: 95124